IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Yeh, et al.

Serial No.:

Initial Filing

Filing Date:

January 14, 2004

For:

Nonvolatile Semiconductor Memory and Operating

Method of the Memory

Group Art No.:

Unassigned

Examiner:

Unassigned

Attorney Docket No.:

87092291.242025

Commissioner of Patents and Trademarks P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97

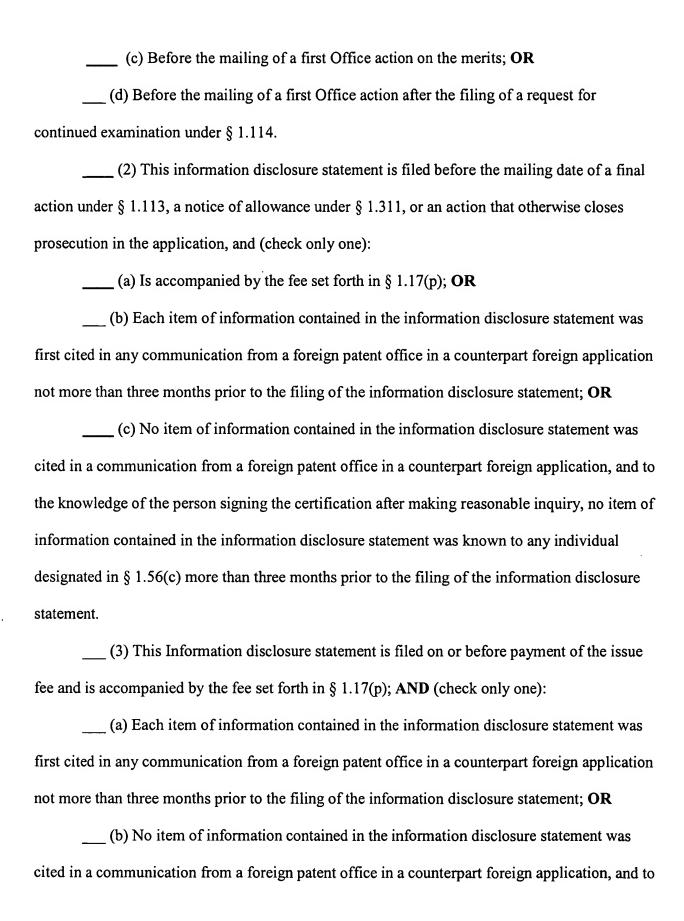
Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant(s) hereby notify the U.S. Patent and Trademark Office of the document(s) which is/are listed on the attached Forms PTO-SB/08A.

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	<u>X</u>	(a)	Within	three	months	of the	filing	date o	of a	national	application	other	than a
contir	nued pro	seci	ıtion a _l	pplica	tion und	ler § 1.	.53(d);	OR					

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the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant(s) does/do not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Please charge any necessary fee or credit any overpayment in connection with this Information Disclosure Statement to Deposit Account No. 13-0480 (please reference 87092291.242025).

Respectfully submitted,

Brian C. McCormack

Reg. No. 36,601

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January 14, 2004 **BAKER & McKENZIE**2300 Trammell Crow Center
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	Substitute for form 1449/PTO	Complete if Known				
		Application Number	Initial Filing			
	INFORMATION DISCLOSURE	Filing Date	01/13/2004			
		First Named Inventor	Yeh			
	STATEMENT BY APPLICANT	Art Unit	Unassigned			
(Use as many sheets as necessary)		Examiner Name	Unassigned			
	Shoot 1 of 2	Attorney Docket Number	87092291 242025			

Examiner Cite No.1		Document Number Number-Kind Code ^{2 (# known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		^{US-} 5,768,192	06-16-1998	Eitan		
		^{US-} 6,331,953	12-18-2001	Wang, et al.		
		US- 6,381,179	04-30-2002	Derhacobian, et al.		
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	FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages				
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INFORMA	TION DIS	CLOSURE	Filing Dat	01/13/2004		
STATEMENT BY APPLICANT			First Named Inventor	Yeh		
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Sheet 2	of	2	Attorney Docket Number	87092291.242025		

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		YEH, et al., PHINES: A Novel Low Power Program/Erase, Small Pitch, 2-Bit per Cell Flash Memory, IEEE, 2002.	

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